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APPLICANT **Masahiko YOSHIMOTO et al.**	
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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes	Translation No
/F.A./	JP H09-009251	01/10/1997	Japan			X	
/F.A./	JP 2002-010271	01/11/2002	Japan			X	
/F.A./	JP H10-248063	09/14/1998	Japan			X	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

/F.A./	"An LSI for V_{DD}.Hopping and MPEG4 System Based on the Chip" Hiroshi Kawaguchi, Gang Zhang, *Sengsoo Lee, and Takayasu Sakurai, Center for Collaborative Research, University of Tokyo 7-22-1, Roppongi, Minato-ku, Tokyo, 106-8558, JAPAN *Department of Information Electronics, Ewha University 11-1 Daehyun-dong, Seodaemun-gu, Seoul, KOREA
/F.A./	A Dynamic Computation Resource Scalable Algorithm for Software Video CODEC, Fuminori OSAKO, Yoshiyuki YASHIMA, Hiroshi KOTERA, Hiroshi WATANABE, and Kazunori SHIMAMURA

EXAMINER /Freshteh Aghdam/	DATE CONSIDERED 05/06/2010

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